Code	**ECE582**
Name	**Parallel Archictures**
Hour per week	3 (3 Theory + 0 Practice)
Credit	3
ECTS	10
Level/Year	Graduate
Semester	Springs
Type	Elective
Prerequisites	-
Content	The main objective of the course is to build a strong understanding of the fundamentals of the architecture of parallel computers and the tradeoffs made in their design. Parallel computers are now almost everywhere and different types of parallelisms are exploited in the computer hardware. These parallelisms, such as, multi-core architectures, parallel memory systems, vector architectures, dataflow machines, and interconnection networks will be explained in the class.